Applicants:

EDLIS, Ofir et al.

Serial No.:

09/778,818

Attorney Docket No.:

Assignee:

P-3309-US Intel Corporation

## Amendments to the Claims:

The following Listing of Claims replaces all prior versions and Listings of Claims in the application:

## **Listing of Claims:**

1. (Currently Amended) A method comprising:

performing processing operations at a first clock rate during at least part of a first time period in which signals are received and stored by a Radio Frequency module;

and

performing background processing operations of at least a portion of said received

signals at a second, faster clock rate during at least part of a second time period in

which said Radio Frequency module is de-activated.

2. (Previously Presented) The method according to claim 1 wherein performing

background processing operations comprises processing spread spectrum signals.

3. (Previously Presented) The method according to claim 1 wherein performing

background processing operations comprises processing Code Division Multiple

Access (CDMA) information.

4. (Previously Presented) The method according to claim 1 wherein performing

background processing operations comprises performing at least one of

synchronizing pseudorandom noise (PN) offset of said received signals, searching for

at least one neighboring communications cell and searching for at least one candidate

communications cell.

2

Applicants: Serial No.: EDLIS, Ofir et al.

09/778,818

Attorney Docket No.: Assignee:

P-3309-US

Intel Corporation

5. (Previously Presented) The method according to claim 4 wherein synchronizing comprises detecting a current pseudorandom noise (PN) offset of said received signals, and, if different from a previous PN offset, shifting to the current PN offset.

- 6. (Previously Presented) The method according to claim 1 comprising receiving a carrier during at least part of said second time period.
- (Previously Presented) The method according to claim 1 comprising receiving said received signals in at least one wake period of a slotted mode.
- (Previously Presented) The method according to claim 7 comprising reducing the power consumed during said at least one wake period after recording said received signals.

## 9. (Cancelled)

- 10. (Previously Presented) A receiver comprising:
  - a Radio Frequency (RF) module adapted to store at least a portion of received signals during a first time period and to be de-activated during a second time period; and a processor adapted to operate at a first clock rate during at least part of said first time period, and to perform background processing of at least said portion of received signals at a second, faster clock rate during at least part of said second time period.
- 11. (Previously Presented) The receiver according to claim 10 wherein said RF module comprises a memory device adapted for storing therein said portion of received signals, and said processor comprises a digital processing unit, wherein said memory device is adapted to input said portion of received signals to said digital processing unit.

Applicants: Serial No.: EDLIS, Ofir et al.

09/778,818

Attorney Docket No.:

Assignee:

P-3309-US

Intel Corporation

12. (Previously Presented) The receiver according to claim 10 wherein said RF module comprises a memory device adapted for storing therein said portion of received signals, and said processor comprises a rake receiver and search engine, wherein said memory device is adapted to input said portion of received signals to said rake receiver and search engine.

- 13. (Previously Presented) The receiver according to claim 11 comprising a sampling unit adapted to receive said portion of received signals and to input said portion of received signals to said memory device.
- 14. (Previously Presented) The receiver according to claim comprising a sampling unit adapted to receive said portion of received signals and to input said portion of received signals to said memory device.
- 15. (Previously Presented) A cellular communication system comprising:
  a Radio Frequency (RF) module adapted to store at least a portion of received signals
  during a first time period and to be de-activated during a second time period; and
  a processor adapted to operate at a first clock rate during at least part of said first
  time period, and to perform background processing of at least said portion of
  received signals at a second, faster clock rate during at least part of said second time
  period.
- 16. (Previously Presented) The cellular communication system according to claim 15 wherein said RF module comprises a memory device adapted for storing therein said portion of received signals, and said processor comprises a digital processing unit, wherein said memory device is adapted to input said portion of received signals to said digital processing unit.

Applicants: Serial No.: EDLIS, Ofir et al.

09/778,818

Attorney Docket No.:

Assignee:

P-3309-US

Intel Corporation

17. (Previously Presented) The cellular communication system according to claim 15 wherein said RF module comprises a memory device adapted for storing therein said portion of received signals, and said processor comprises a rake receiver and search engine, wherein said memory device is adapted to input said portion of received signals to said rake receiver and search engine.

- 18. (Previously Presented) The cellular communication system according to claim 16 comprising a sampling unit adapted to receive said portion of received signals and to input said portion of received signals to said memory device.
- 19. (Previously Presented) The cellular communication system according to claim 17 comprising a sampling unit adapted to receive said portion of received signals and to input said portion of received signals to said memory device.